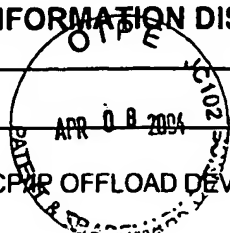


U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/729,111
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> 		Filing date: December 5, 2003
		Inventors: Starr et al.
		Group Art Unit: 2661
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING		Examiner name: <del>Unknown</del> W, 1504
		Attorney Docket No.: ALA-026

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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	B						
	C						
	D						
	E						
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	I						
	J						
	K						

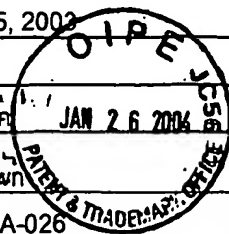
Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
	L						
	M						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
<i>RM</i>	N	EE Times article entitled "Ethernet Interconnect Outpacing Infiniband At Intel", by Rick Merritt, 3 pages, September 12, 2002.
	O	
	P	
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Examiner <i>Robert W. Wilson</i>	Date Considered <i>5/10/05</i>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/729,111
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing date: December 5, 2002
		Inventors: Starr, et al.
		Group Art Unit: <del>Unknown</del> 266
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING		Examiner name: <del>unknown</del> WILSON
		Attorney Docket No.: ALA-026



**OTHER ART—NON PATENT LITERATURE DOCUMENTS**

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
RNV	1	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.
RNV	2	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.
RNV	3	Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97.
RNV	4	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.
RNV	5	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University. 13 pages, printed 12/21/98.
RNV	6	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
RNV	7	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
RNV	8	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996, 15 pages.
RNV	9	Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.
RNV	10	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
RNV	11	iReady News Archives article entitled "iReady Rounding Out Management Team with Two Key Executives", <a href="http://www.ireadyco.com/archives/keyexec.html">http://www.ireadyco.com/archives/keyexec.html</a> , 2 pages, printed 11/28/98.
RNV	12	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release October, 1998, 3 pages, printed 11/28/98.
RNV	13	Internet pages from iReady Products, web site <a href="http://www.ireadyco.com/products.html">http://www.ireadyco.com/products.html</a> , 2 pages, downloaded 11/25/98.
RNV	14	iReady News Archives, Toshiba, iReady shipping Internet chip, 1 page, printed 11/25/98.
RNV	15	Interprophet article entitled "Technology", <a href="http://www.interprophet.com/technology.html">http://www.interprophet.com/technology.html</a> , 17 pages, printed 3/1/00.

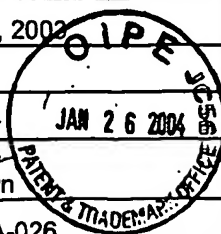
Examiner Robert W. Wilson

Date Considered 5/16/05

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U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/729,111
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing date: December 5, 2002
		Inventors: Starr, et al.
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING		Group Art Unit: <sup>2661</sup> unknown
		Examiner name: <sup>Wilson</sup> unknown
		Attorney Docket No.: ALA-026
<b>OTHER ART—NON PATENT LITERATURE DOCUMENTS</b>		
*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
RNN	16	iReady Corporation, article entitled "The I-1000 Internet Tuner", 2 pages, date unknown.
RNN	17	iReady article entitled "About Us Introduction", Internet pages from <a href="http://www.iReadyco.com/about.html">http://www.iReadyco.com/about.html</a> , 3 pages, printed 11/25/98.
RNN	18	iReady News Archive article entitled "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA, November 20, 1997. 2 pages, printed 11/2/98.
RNN	19	iReady News Archive article entitled "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," Santa Clara, CA and Chiba, Japan, October 26, 1998. 2 pages, printed 11/2/98.
RNN	20	NEWSwatch article entitled "iReady internet Tuner to Web Enable Devices", Tuesday, November 5, 1996, printed 11/2/98, 2 pages.
RNN	21	EETimes article entitled "Tuner for Toshiba, Toshiba Taps iReady for Internet Tuner", by David Lammers, 2 pages, printed 11/02/98.
RNN	22	"Comparison of Novell Netware and TCP/IP Protocol Architectures", by J.S. Carbone, 19 pages, printed 4/10/98.
RNN	23	Adaptec article entitled "AEA-7110C-a DuraSAN product", 11 pages, printed 10/1/01.
RNN	24	iSCSI HBA article entitled "iSCSI and 2Gigabit fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.
RNN	25	iSCSI HBA article entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channel HBA", 6 pages, printed 10/01/01.
RNN	26	iSCSI.com article entitled "iSCSI Storage", 2 pages, printed 10/01/01.
RNN	27	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998, 17 pages.
RNN	28	iReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", Santa Clara, CA, and Tokyo, Japan, October 14, 1998, printed 11/2/98, 3 pages.
RNN	29	Internet pages of InterProphet entitled "Frequently Asked Questions", by Lynne Jolitz, printed 6/14/00, 4 pages.
RNN	30	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
Examiner <u>Robert N. Wilson</u>		Date Considered <u>5/10/05</u>
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>		

U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/729,111
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing date: December 5, 2002
		Inventors: Starr, et al.
		Group Art Unit: <sup>2661</sup> unknown
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING		Examiner name: <sup>W. S. S.</sup> unknown
		Attorney Docket No.: ALA-026



**OTHER ART—NON PATENT LITERATURE DOCUMENTS**

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
RTH	31	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
RTH	32	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.
RTH	33	Adaptec article entitled "EtherStorage White Paper", 7 pages, printed 7/19/00.
RTH	34	CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.
RTH	35	Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.
RTH	36	CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.
RTH	37	Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.
RTH	38	Internet pages entitled "Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, printed 6/5/97, 15 pages.
RTH	39	Jato Technologies article entitled "Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98, 13 pages.
RTH	40	EETimes article entitled "Enterprise System Uses Flexible Spec, dated August 10, 1998, printed 11/25/98, 3 pages.
RTH	41	Internet pages entitled "Smart Ethernet Network Interface Cards", which Berend Ozceri is developing, printed 11/25/98, 2 pages.
RTH	42	Internet pages of Xaqti corporation entitled "GigaPower Protocol Processor Product Review," printed 11/25/99, 4 pages.
RTH	43	Internet pages entitled "DART: Fast Application Level Networking via Data-Copy Avoidance," by Robert J. Walsh, printed 6/3/99, 25 pages.

Examiner <i>Robert N. Miller</i>	Date Considered <i>5/15/05</i>
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U.S. Department of Commerce, Patent and Trademark Office					Application No.: 10/729,111		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>					Filing date: December 5, 2003		
					Inventors: Starr, et al.		
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING					Group Art Unit: <sup>2661</sup> unknown		
					Examiner name: <sup>Wilson</sup> unknown		
					Attorney Docket No.: ALA-026		
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
RHH	A	2003/0165160	9/4/03	Minami et al.	—	—	
RHH	B	2003/0066011	4/11/02	Oren	—	—	
RHH	C	2001/0053148	12/20/01	Bilic et al.	—	—	
RHH	D	2001/0048681	12/6/01	Bilic et al.	—	—	
RHH	E	2001/0025315	01/10/01	Jolitz	—	—	
RHH	F	2001/0014954	8/16/01	Purcell et al.	—	—	
RHH	G	2001/0014892	8/16/01	Gaither et al.	—	—	
RHH	H	2001/0013059	8/9/01	Dawson et al.	—	—	
RHH	I	2001/0004354	01/10/01	Jolitz			
RHH	J	4,366,538	12/28/82	Johnson et al.	364	200	
RHH	K	4,589,063	5/13/86	Shah et al.	710	8	
RHH	L	4,991,133	5/13/86	Davis et al.	364	900	
RHH	M	5,056,058	10/8/91	Hirata et al.	709	230	
RHH	N	5,056,058	10/8/91	Hirata, et al.	364	900	
RHH	O	5,058,110	10/15/91	Beach et al.	370	85.6	
RHH	P	5,097,442	3/17/92	Ward et al.	365	78	
RHH	Q	5,163,131	11/10/92	Row et al.	395	200	
RHH	R	5,212,778	5/18/93	Dally et al.	395	400	
RHH	S	5,280,477	1/18/94	Trapp	370	85.1	
RHH	T	5,289,580	2/22/94	Latif et al.	395	275	
RHH	U	5,303,344	4/12/94	Yokoyama et al.	395	200	
RHH	V	5,412,782	5/2/95	Hausman et al.	395	250	
RHH	W	5,448,566	9/5/95	Richter et al.	370	94.1	
RHH	X	5,485,579	1/16/96	Hitz et al.	395	200.12	
RHH	Y	5,506,966	4/9/96	Ban	395	250	
RHH	Z	5,511,169	4/23/96	Suda	395	280	
RHH	AA	5,548,730	8/20/96	Young et al.	395	280	
RHH	AB	<del>5,588,121</del> 5,588,121	12/24/96	Reddin et al.	395	200.15	
Examiner <u>Robert W. Wilson</u>				Date Considered <u>5/10/05</u>			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							

U.S. Department of Commerce, Patent and Trademark Office					Application No.: 10/729,111		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>					Filing date: December 5, 2005		
					Inventors: Starr, et al.		
					Group Art Unit: unknown		
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING					Examiner name: Wilso A. [unclear]		
					Attorney Docket No.: ALA-026		
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
RNN	AC	5,566,170	10/15/96	Bakke et al.	370	60	
RNN	AD	5,590,328	12/31/96	Seno et al.	395	675	
RNN	AE	5,592,622	1/7/97	Isfeld et al.	395	200.02	
RNN	AF	5,629,933	5/13/97	Delp et al.	370	411	
RNN	AG	5,634,099	5/27/97	Andrews et al.	395	200.07	
RNN	AH	5,634,127	5/27/97	Cloud et al.	395	680	
RNN	AI	5,642,482	6/24/97	Pardillos	395	200.2	
RNN	AJ	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
RNN	AK	5,671,355	9/23/97	Collins	395	200.2	
RNN	AL	5,678,060	10/14/97	Yokoyama et al.	709	212	
RNN	AM	5,692,130	11/25/97	Shobu et al.	395	200.12	
RNN	AN	5,699,317	12/16/97	Sartore et al.	395	230.06	
RNN	AO	5,701,434	12/23/97	Nakagawa	395	484	
RNN	AP	5,701,516	12/23/97	Cheng et al.	395	842	
RNN	AQ	5,749,095	5/5/98	Hagersten	711	141	
RNN	AR	5,751,715	5/12/98	Chan et al.	370	455	
RNN	AS	5,752,078	5/12/98	Delp et al.	395	827	
RNN	AT	5,758,084	5/26/08	Silverstein et al.	395	200.58	
RNN	AU	5,758,089	5/26/08	Gentry et al.	395	200.64	
RNN	AV	5,758,186	5/26/08	Hamilton et al.	395	831	
RNN	AW	5,758,194	5/26/08	Kuzma	395	886	
RNN	AX	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
RNN	AY	5,778,013	7/7/98	Jedwab	714	807	
RNN	AZ	5,790,804	8/4/98	Osborne	709	245	
RNN	BA	5,794,061	08/11/98	Hansen et al.	395	800.01	
RNN	BB	5,802,580	09/01/98	McAlpice	711	149	
RNN	BC	5,809,328	09/15/98	Nogales et al.	395	825	
RNN	BD	5,812,775	9/22/98	Van Seeters et al.	395	200.43	
Examiner Robert N. Wilso				Date Considered 5/10/05			
*EXAMINER: Initial if reference considered; whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/729,111
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Filing date: December 5, 2003
	Inventors: Starr, et al.
	Group Art Unit: <sup>2661</sup> Unknown
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING	Examiner name: <sup>Wilson</sup> unknown
	Attorney Docket No.: ALA-026

## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
RNN	BE	5,815,646	09/29/98	Purcell et al.	395	163	
RNN	BF	5,878,225	3/2/99	Bilansky et al.	395	200.57	
RNN	BG	5,913,028	6/15/99	Wang, et al.	395	200.33	
RNN	BH	5,930,830	7/27/99	Mendelson et al.	711	171	
RNN	BI	5,931,918	08/03/99	Row et al.	709	300	
RNN	BJ	5,935,205	08/10/99	Murayama et al.	709	216	
RNN	BK	5,937,169	08/10/99	Connery et al.	395	200.8	
RNN	BL	5,941,969	08/24/99	Ram et al.	710	128	
RNN	BM	5,941,972	08/24/99	Hoese et al.	710	129	
RNN	BN	5,950,203	09/07/99	Stakuis et al.	707	10	
RNN	BO	5,970,804	8/4/98	Osborne	395	200.75	
RNN	BP	5,991,299	11/23/99	Radogna et al.	370	392	
RNN	BQ	5,996,024	11/30/99	Blumenau	709	301	
RNN	BR	6,005,849	12/21/99	Roach et al.	370	276	
RNN	BS	6,009,478	12/28/99	Panner, et al.	710	5	
RNN	BT	6,016,513	01/18/00	Glen H. Lowe	709	250	
RNN	BU	6,021,446	02/01/00	Gentry et al.	709	303	
RNN	BV	6,026,452	02/15/00	William Michael Pitts	710	56	
RNN	BW	6,034,963	3/7/00	Minami et al.	370	401	
RNN	BX	6,038,562	3/14/00	Anjur et al.	707	10	
RNN	BY	6,044,438	03/28/00	Howard Thomas Olnowich	711	130	
RNN	BZ	6,047,356	04/04/00	Anderson et al.	711	129	
RNN	CA	6,057,863	05/02/00	Sompong P. Olarig	345	520	
RNN	CB	6,061,368	5/9/00	Hitzelberger	370	537	
RNN	CC	6,065,096	05/16/00	Day et al.	711	114	
RNN	CD	6,070,200	5/30/00	Gates et al.	710	20	
RNN	CE	6,101,555	8/8/00	Goshey et al.	709	321	
RNN	CF	6,141,705	10/31/00	Anand et al.	710	15	

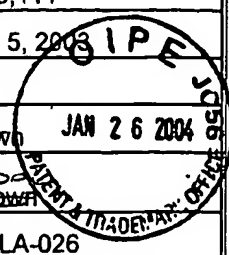
Examiner Robert N. WilsonDate Considered 5/10/05

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office					Application No.: 10/729,111		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>					Filing date: December 5, 2003		
					Inventors: Starr, et al.		
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING					Group Art Unit: <sup>2461</sup> unknown		
					Examiner name: <sup>W. Iso</sup> unknown		
					Attorney Docket No.: ALA-026		
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
RNN	CG	6,145,017	11/7/00	Ghaffari	710	5	
RNN	CH	6,157,955	12/5/00	Narad et al.	709	228	
RNN	CI	6,172,980	1/9/01	Flanders et al.	370	401	
RNN	CJ	6,173,333	07/18/97	Jolitz et al.			
RNN	CK	6,202,105	3/13/01	Gates et al.	710	20	
RNN	CL	6,226,680	05/01/01	Boucher et al.	709	230	
RNN	CM	6,246,683	06/12/01	Connery et al.	370	392	
RNN	CN	6,247,060	06/12/01	Boucher et al.	709	238	
RNN	CO	6,279,051	8/21/01	Gates et al.	710	20	
RNN	CP	6,298,403	10/2/01	Suri et al.	710	100	
RNN	CQ	6,334,153	12/25/01	Boucher et al.	709	230	
RNN	CR	6,345,301	02/05/02	Burns et al.	709	230	
RNN	CS	6,345,301	02/05/02	Burns et al.	709	230	
RNN	CT	6,356,951	03/12/02	Gentry et al.	709	250	
RNN	CU	6,389,468	05/14/02	Muller et al.	709	226	
RNN	CV	6,389,479	5/14/02	Boucher	709	243	
RNN	CW	6,421,742	7/16/02	Tillier	710	1	
RNN	CX	6,427,169	07/30/02	Elzur	709	224	
RNN	CY	6,434,651	08/13/02	Gentry, Jr.	710	260	
RNN	CZ	6,449,656	09/10/02	Elzur et al.	709	236	
RNN	DA	6,453,360	09/17/02	Muller et al.	709	250	
RNN	DB	6,480,489	11/12/02	Muller et al.	370	389	
RNN	DC	6,526,446	2/25/03	Yang et al.	709	230	
	DD						
	DE						
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Examiner <i>Robert N. Wilson</i>		Date Considered 5/10/05					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>							



U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/729,111
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Filing date: December 5, 2003
	Inventors: Starr, et al.
	Group Art Unit: <sup>2CC1</sup> unknown
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING	Examiner name: <sup>W. 132</sup> unknown
	Attorney Docket No.: ALA-026




**Foreign Patent Documents**

							Translation	
*Examiner Initial		Document Number	Date	Country	Class	Subclass	Yes	No
RNN	A	WO 98/50852	11/12/98	PCT/US98/0871	—	—		
RNN	B	WO 99/04343	1/28/99	PCT/US98/14729	—	—		
RNN	C	WO 99/65219	12/16/99	PCT/US/99/13184	—	—		
RNN	D	WO 00/13091	3/09/00	PCT/US98/24943	—	—		
RNN	E	WO 01/04770	1/18/01	PCT/US00/18939	—	—		
RNN	F	WO 01/05107	1/18/01	PCT/US00/19006	—	—		
RNN	G	WO 01/05116	1/18/01	PCT/US00/19243	—	—		
RNN	H	WO 01/05123	1/18/01	PCT/US00/18976	—	—		
RNN	I	WO 01/40960	6/07/01	PCT/US00/32660	—	—		
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Examiner <i>Robert H. Wilson</i>	Date Considered <i>5/10/05</i>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office					Application No.: 10/729,111		
<b>2<sup>ND</sup> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>					Filing date: December 5, 2003		
					Inventors: Starr et al.		
 <b>TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING</b>					Group Art Unit: 2661		
					Examiner name: <del>Unknown</del> Wilson		
					Attorney Docket No.: ALA-026		

U.S. Patent Documents							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
RWN	A	5,598,410	1/28/1997	Stone	370	469	
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

Foreign Patent Documents							Translation	
Document Number	Date	Country	Class	Subclass	Yes	No		
L								
M								

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
N	
O	
P	
Q	

Examiner <i>Robert W. Wilson</i>	Date Considered <i>5/10/05</i>
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/729,111
4 <sup>th</sup> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: December 5, 2003
	Inventors: Starr et al.
	Group Art Unit: 2661
TCP/IP OFFLOAD DEVICE WITH REDUCED SEQUENTIAL PROCESSING	Examiner name: <del>Unknown</del> Wilson
	Attorney Docket No.: ALA-026

## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>RTH</i>	A	5,524,250	6/4/96	Chesson et al.	395	775	
<i>RTH</i>	B	5,619,650	4/8/97	Bach et al.	395	200.01	
<i>RTH</i>	C	5,727,142	3/10/98	Chen	395	181	
<i>RTH</i>	D	5,802,258	9/1/98	Chen	395	182.08	
<i>RTH</i>	E	5,898,713	4/27/99	Melzer et al.	371	53	
<i>RTH</i>	F	6,021,507	2/1/00	Chen	714	2	
<i>RTH</i>	G	6,047,323	4/4/00	Krause	709	227	
	H						

## OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
<i>RTH</i>	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 <sup>th</sup> Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
<i>RTH</i>	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 <sup>th</sup> Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
<i>RTH</i>	3	Chesson et al., IEEE Symposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
<i>RTH</i>	4	Macleane et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.
<i>RTH</i>	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
<i>RTH</i>	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
<i>RTH</i>	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.
<i>RTH</i>	8	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
<i>RTH</i>	9	Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.

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